

Notice of References Cited	Application/Control No. 10/694,919		Applicant(s)/Patent Under Reexamination HWANG ET AL.	
	Examiner Tuyen To		Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,480,023	11-2002	Kaviani, Alireza S.	326/38
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Vemuri et al., "BDD-Based Logic Synthesis for LUT-Based FPGAs", ACM Transactions on Design Automation of Electronic systems, Vol. 7, No.4, Oct. 2002, Pages 501-525.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.